

REPLACE MENT DRAWING SHEET

Methods and Devices for Improving the Switching Times of PLLs

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1/1

FIG. 1 (PRIOR ART)

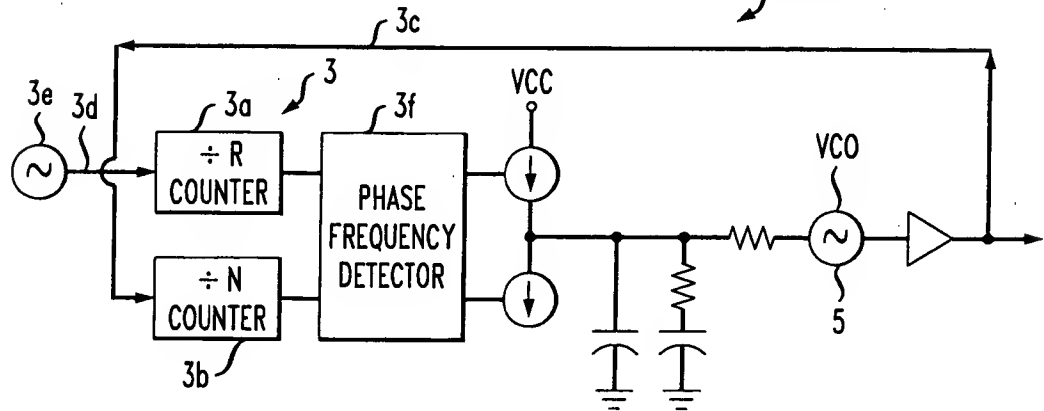


FIG. 2

